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BUR920010040US2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jeffrey P. Gambino, et al.

Examiner: Shrinivas H. Rao

Serial No: 10/695,335

Art Unit: 2814

Filed: October 28, 2003

Docket: BUR920010040US2

For: FIN FIELD EFFECT TRANSISTOR  
WITH SELF-ALIGNED GATE

Date: December 1, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450AMENDMENT AND RESPONSE

Sir:

In response to the Office Action dated September 27, 2004, Applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

## CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8

I certify that this correspondence of 4 pages is being transmitted by facsimile to the United States Patent and Trademark Office to: Examiner Shrinivas H. Rao, Art Unit 2814 at facsimile number 703 872-9318 on December 1, 2004 by Arnette Dodge

  
(Signature of person transmitting paper)